



**CHENMKO ENTERPRISE CO.,LTD**

**SURFACE MOUNT**

**N-Channel Enhancement Mode Field Effect Transistor**

**VOLTAGE 30 Volts CURRENT 55 Ampere**

**CHM63A3PAPT**

*Lead free devices*

#### APPLICATION

- \* Servo motor control.
- \* Power MOSFET gate drivers.
- \* Other switching applications.

#### FEATURE

- \* Small package. (TO-252A)
- \* Super high dense cell design for extremely low Rds(ON).
- \* High power and current handing capability.

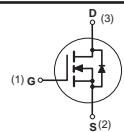
#### CONSTRUCTION

- \* N-Channel Enhancement

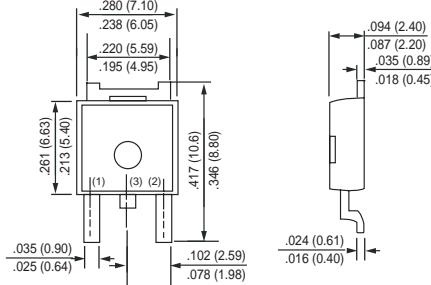
#### MARKING

- \* 63A3

#### CIRCUIT



**TO-252A**



Dimensions in inches and (millimeters)

**TO-252A**

#### Absolute Maximum Ratings

T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	CHM63A3PAPT	Units
V <sub>DSS</sub>	Drain-Source Voltage	30	V
V <sub>GSS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Maximum Drain Current - Continuous	55	A
	- Pulsed (Note 3)	150	
P <sub>D</sub>	Maximum Power Dissipation at T <sub>c</sub> = 25 °C	57	W
T <sub>J</sub>	Operating Temperature Range	-55 to 150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C

Note : 1. Surface Mounted on FR4 Board , t <=10sec  
 2. Pulse Test , Pulse width <= 300us , Duty Cycle <= 2%  
 3. Repetitive Rating , Pulse width limited by maximum junction temperature  
 4. Guaranteed by design , not subject to production testing

#### Thermal characteristics

R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient (Note 1)	50	°C/W
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2006-02

## ELECTRICAL CHARACTERISTIC ( CHM63A3PAPT )

**Electrical Characteristics**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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### OFF CHARACTERISTICS

$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 30 \text{ V}, V_{\text{GS}} = 0 \text{ V}$			1	$\mu\text{A}$
$I_{\text{GSSF}}$	Gate-Body Leakage	$V_{\text{GS}} = 20\text{V}, V_{\text{DS}} = 0 \text{ V}$			+100	nA
$I_{\text{GSSR}}$	Gate-Body Leakage	$V_{\text{GS}} = -20\text{V}, V_{\text{DS}} = 0 \text{ V}$			-100	nA

### ON CHARACTERISTICS (Note 2)

$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250 \mu\text{A}$	1		3	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}, I_D=30\text{A}$		8.5	11	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_D=24\text{A}$		11	14	
$g_{\text{FS}}$	Forward Transconductance	$V_{\text{DS}} = 10\text{V}, I_D = 30\text{A}$		22		S

### SWITCHING CHARACTERISTICS (Note 4)

$Q_g$	Total Gate Charge	$V_{\text{DS}}=15\text{V}, I_D=45\text{A}$ $V_{\text{GS}}=10\text{V}$		33	45	nC
$Q_{\text{gs}}$	Gate-Source Charge			5.5		
$Q_{\text{gd}}$	Gate-Drain Charge			8.2		
$t_{\text{on}}$	Turn-On Time	$V_{\text{DD}}= 15\text{V}$ $I_D = 45\text{A}, V_{\text{GS}}= 10 \text{ V}$ $R_{\text{GEN}}= 24 \Omega$		17	35	nS
$t_r$	Rise Time			18	30	
$t_{\text{off}}$	Turn-Off Time			156	220	
$t_f$	Fall Time			77	140	

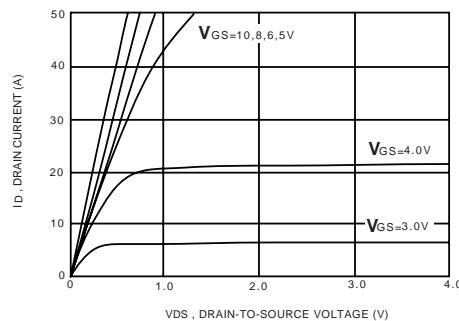
### DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

$I_s$	Drain-Source Diode Forward Current			30	A
$V_{\text{SD}}$	Drain-Source Diode Forward Voltage	$I_s = 30\text{A}, V_{\text{GS}} = 0 \text{ V}$		1.2	V

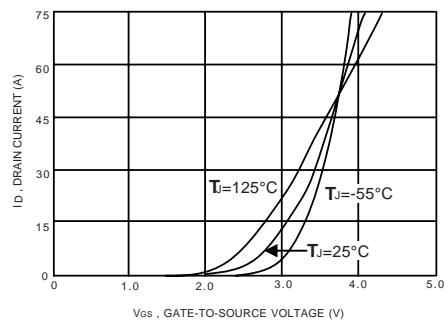
## RATING CHARACTERISTIC CURVES ( CHM63A3PAPT )

### Typical Electrical Characteristics

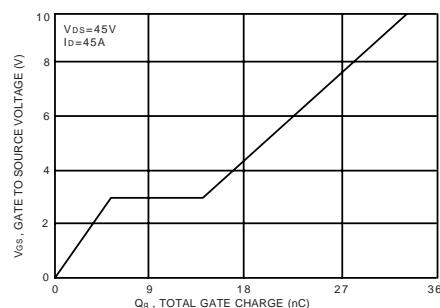
**Figure 1. Output Characteristics**



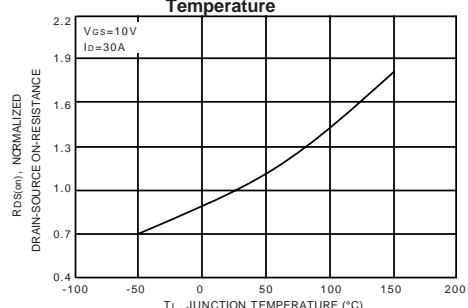
**Figure 2. Transfer Characteristics**



**Figure 3. Gate Charge**



**Figure 4. On-Resistance Variation with Temperature**



**Figure 5. Gate Threshold Variation with Temperature**

